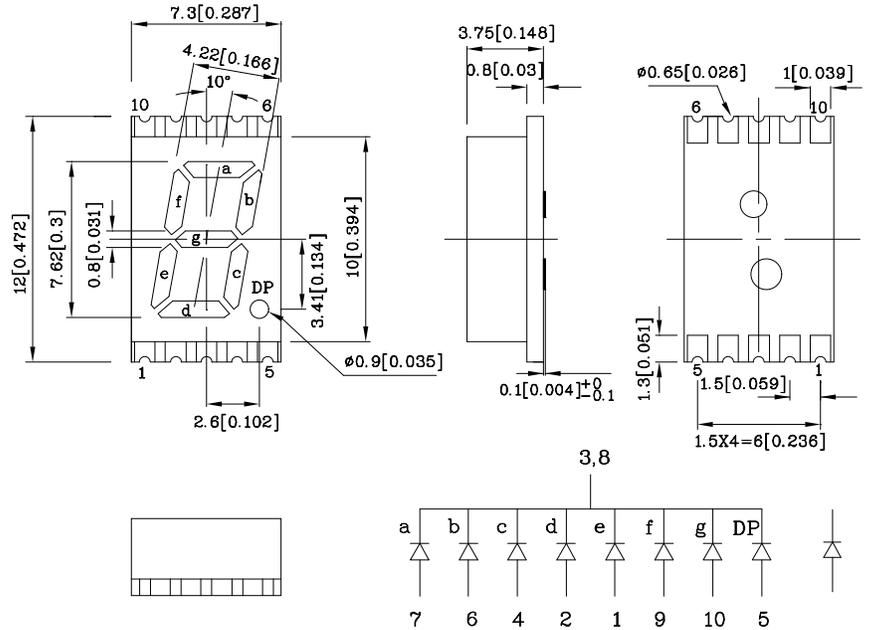


### Features

- 0.3 inch digit height
- Robust package
- Low power consumption
- Standard configuration: Gray face w/ white segments
- Standard Package: 550pcs/ Reel
- MSL (Moisture Sensitivity Level): 2a
- RoHS compliant



### Package Schematics



#### Notes:

1. All dimensions are in millimeters (inches), Tolerance is  $\pm 0.25(0.01)$  unless otherwise noted.
2. Specifications are subject to change without notice.
3. The gap between the reflector and PCB shall not exceed 0.25mm.

Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )		Red (GaAsP/GaP)	Unit
Reverse Voltage	$V_R$	5	V
Forward Current	$I_F$	30	mA
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	$i_{FS}$	160	mA
Power Dissipation	$P_D$	75	mW
Operating Temperature	$T_A$	-40 ~ +85	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-40 ~ +85	

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

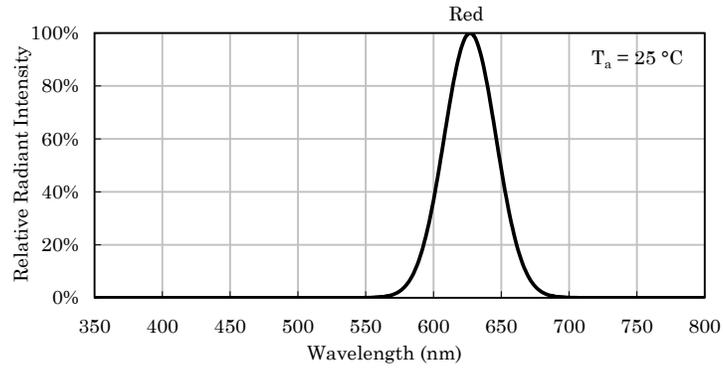
Operating Characteristics ( $T_A=25^\circ\text{C}$ )		Red (GaAsP/GaP)	Unit
Forward Voltage (Typ.) ( $I_F=10\text{mA}$ )	$V_F$	1.9	V
Forward Voltage (Max.) ( $I_F=10\text{mA}$ )	$V_F$	2.3	V
Reverse Current (Max.) ( $V_R=5\text{V}$ )	$I_R$	10	$\mu\text{A}$
Wavelength of Peak Emission CIE127-2007* (Typ.) ( $I_F=10\text{mA}$ )	$\lambda_P$	627*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) ( $I_F=10\text{mA}$ )	$\lambda_D$	617*	nm
Spectral Line Full Width At Half-Maximum (Typ.) ( $I_F=10\text{mA}$ )	$\Delta\lambda$	45	nm
Capacitance (Typ.) ( $V_F=0\text{V}$ , $f=1\text{MHz}$ )	C	15	pF

Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* ( $I_F=10\text{mA}$ ) ucd		Wavelength CIE127-2007* nm $\lambda_P$	Description
			min.	typ.		
XZFU07C	Red	GaAsP/GaP	1400 360*	2490 690*	565*	Common Cathode, Rt. Hand Decimal.

\*Luminous intensity value and wavelength are in accordance with CIE127-2007 standards.

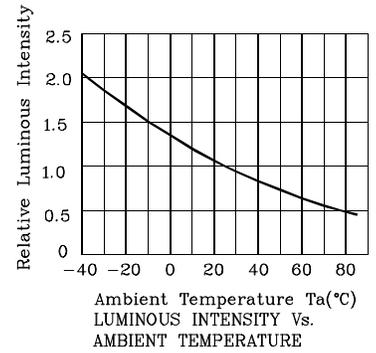
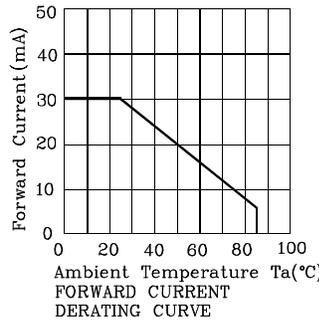
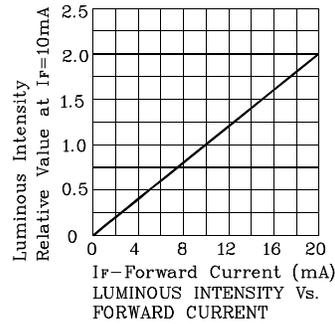
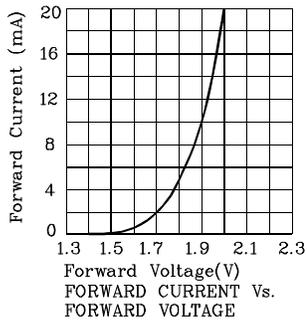
Feb 21, 2019

XDSA9166 V6-X Layout: Maggie L.



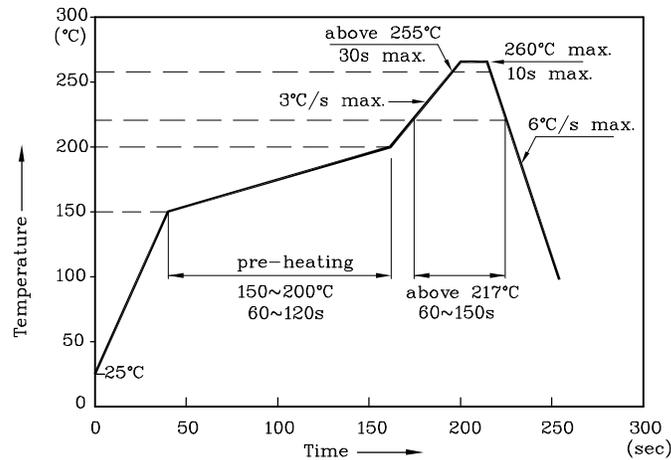
Relative Intensity Vs. CIE Wavelength

◆ Red



**LED is recommended for reflow soldering and soldering profile is shown below.**

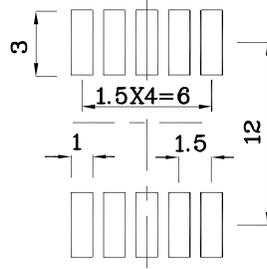
Reflow Soldering Profile for SMD Products (Pb-Free Components)



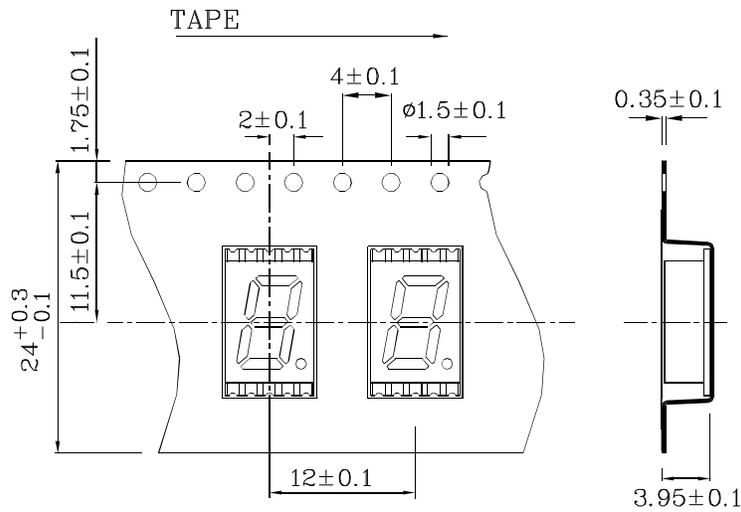
1. All temperatures refer to the center of the package, measured on the package body surface facing up during reflow.
2. Do not apply any stress to the LED during high temperature conditions.
3. Maximum number of soldering passes: 2



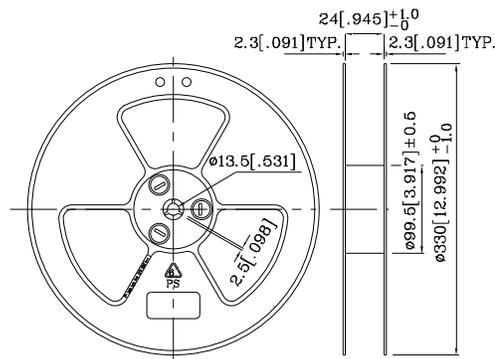
❖ Recommended Soldering Pattern (Units : mm; Tolerance:  $\pm 0.15$ )



❖ Tape Specification (Units : mm)



❖ Reel Dimension



Remarks:

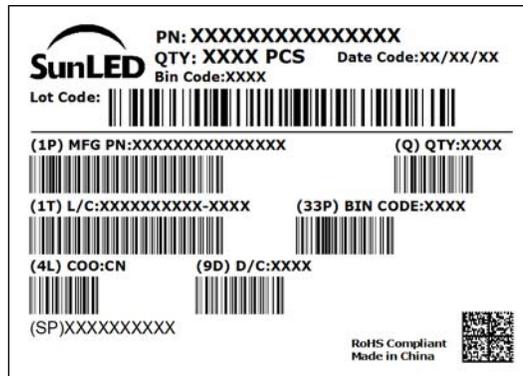
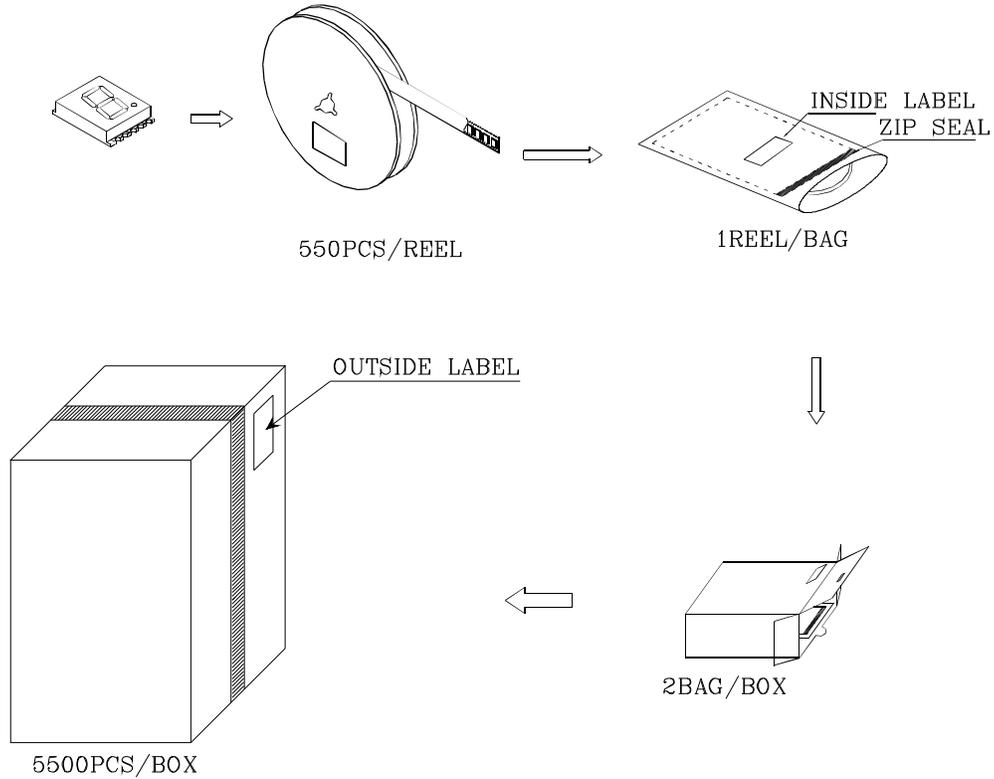
If special sorting is required (e.g. binning based on forward voltage, Luminous intensity / luminous flux, or wavelength), the typical accuracy of the sorting process is as follows:

1. Wavelength:  $\pm 1$ nm
2. Luminous intensity / luminous flux:  $\pm 15\%$
3. Forward Voltage:  $\pm 0.1$ V

Note: Accuracy may depend on the sorting parameters.



**PACKING & LABEL SPECIFICATIONS**



**TERMS OF USE**

1. Data presented in this document reflect statistical figures and should be treated as technical reference only.
2. Contents within this document are subject to improvement and enhancement changes without notice.
3. The product(s) in this document are designed to be operated within the electrical and environmental specifications indicated on the datasheet. User accepts full risk and responsibility when operating the product(s) beyond their intended specifications.
4. The product(s) described in this document are intended for electronic applications in which a person's life is not reliant upon the LED. Please consult with a SunLED representative for special applications where the LED may have a direct impact on a person's life.
5. The contents within this document may not be altered without prior consent by SunLED.
6. When any special process such as potting is required for LED assembly, please consult with SunLED representative before proceeding.
7. Additional technical notes are available at <https://www.SunLEDusa.com/TechnicalNotes.asp>