

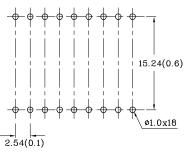
## Features

- $\bullet$  Low power consumption
- $\bullet$  Robust package
- I.C. Compatible
- Standard configuration: Gray face w/ white segments
- Optional black face provides superior color

contrast

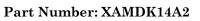
• RoHS Compliant

Recommended PCB Layout

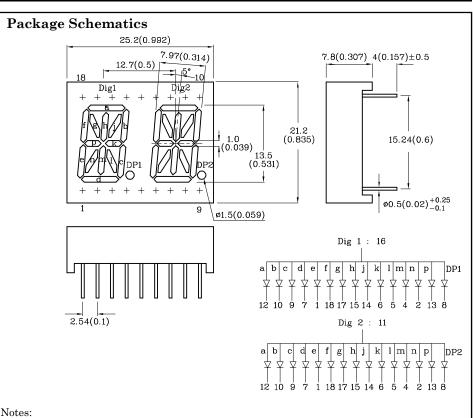


ATT OBSERVE FOR ELEC DIS SE

ATTENTION OBSERVE PRECAUTIONS FOR HANDLING ELECTROSTATIC DISCHARGE SENSITIVE DEVICES



13.5 mm (0.531") 14 Segment Dual Digit Alphanumeric Display



All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01")unless otherwise noted.
Specifications are subject to change without notice.

Absolute Maximum Ratings (T <sub>A</sub> =25°C)		Red (AlGaInP)	Unit	
Reverse Voltage	$V_{R}$	5	V	
Forward Current	$I_{\rm F}$	30	mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	ifs	185	mA	
Power Dissipation	PD	75	mW	
Operating Temperature	$T_{\rm A}$	$-40 \sim +85$	°C	
Storage Temperature	Tstg	$-40 \sim +85$		
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds			

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

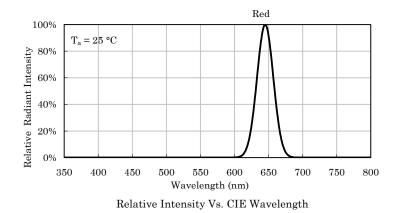
Operating Characteristics (T <sub>A</sub> =25°C)		Red (AlGaInP)	Unit
Forward Voltage (Typ.) (I <sub>F</sub> =10mA)	$V_{\rm F}$	1.85	V
Forward Voltage (Max.) (I <sub>F</sub> =10mA)	$V_{\rm F}$	2.35	V
Reverse Current (Max.) (V <sub>R</sub> =5V)	$I_{R}$	10	μΑ
Wavelength of Peak Emission CIE127-2007* (Typ.) (I <sub>F</sub> =10mA)	λP	645*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) (I <sub>F</sub> =10mA)	λD	630*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I <sub>F</sub> =10mA)	Δλ	28	nm
Capacitance (Typ.) (V <sub>F</sub> =0V, f=1MHz)	С	35	pF

Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* (I <sub>F</sub> =10mA) ucd		CIE127-2007*		Wavelength CIE127-2007* nm λΡ	Description
			min.	typ.				
XAMDK14A2	Red	AlGaInP	31000 9000*	67990 22990*	645*	Common Anode, Rt.Hand Decimal.		

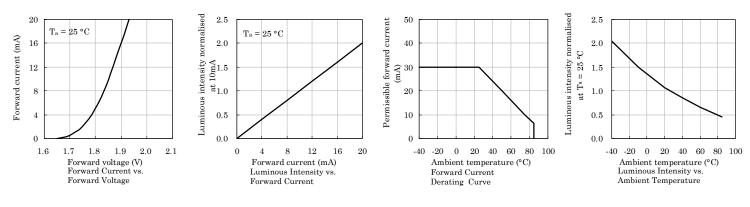
\*Luminous intensity value and wavelength are in accordance with CIE127-2007 standards. Mar 28,2022

XDSB9504 V1-X Layout: Maggie L.

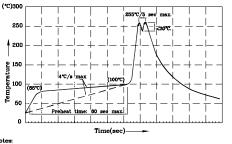








Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



Access I.Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C 2.Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max)

 Peak wave soldering temperature between 245°C ~ 255°C for 3 secmax).
Do not apply stress to the epoxy resin while the temperature is a 4-Pixtures should not incur stress on the component when mounting during soldering process.
SAC 305 solder alloy is recommended.
No more than one wave soldering pass.
During wave soldering, the PCB top-surface temperature should be kept below 105°C. while the temperature is above component when mounting and 85°C

Remarks:

If special sorting is required (e.g. binning based on forward voltage,

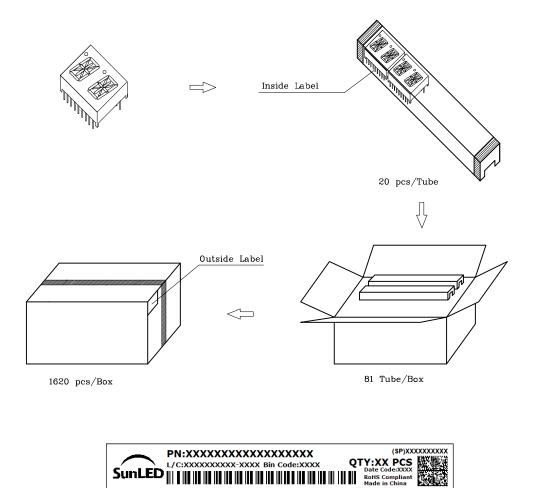
luminous intensity / luminous flux, or wavelength),

the typical accuracy of the sorting process is as follows:

- 1. Wavelength: +/-1nm
- 2. Luminous Intensity / Luminous Flux: +/-15%
- 3. Forward Voltage: +/-0.1V
- Note: Accuracy may depend on the sorting parameters.



## **PACKING & LABEL SPECIFICATIONS**



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- 7. Additional technical notes are available at https://www.SunLEDusa.com/TechnicalNotes.asp